

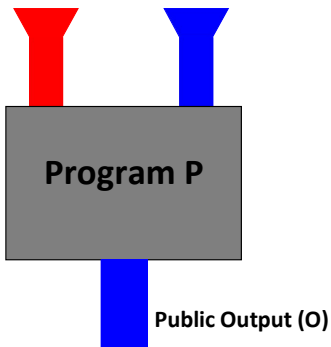
Quantifying Information Leaks via Model Counting Modulo Theories

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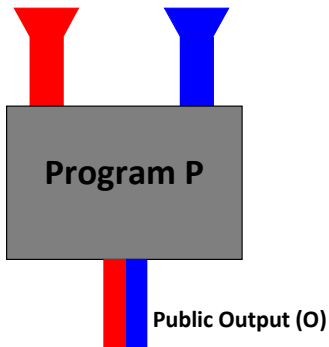
April 28, 2015

Secret input (H) Public input (L)



Non-interference

Secret input (H) Public input (L)

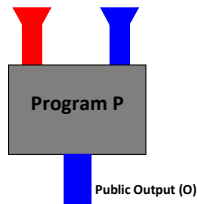


Information leaked

```
int check(int H, int L){  
  int O;  
  if (L == H)  
    O = ACCEPT;  
  else O = REJECT;  
  return O;  
}
```

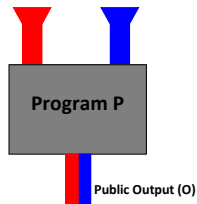
password check

Secret input (H) Public input (L)



Non-interference

Secret input (H) Public input (L)



Information leaked

Leaks = Secrecy before observing - Secrecy after observing

$$\Delta_E(X_H) = E(X_H) - E(X_H|X_O)$$

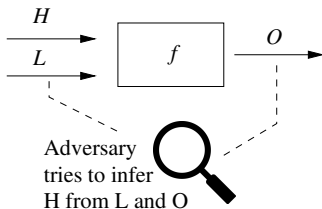
Theorem of Channel Capacity

$$\Delta_E(X_H) \leq \log_2(|O|)$$

- has been proved for Shannon entropy and Rényi's min-entropy
- holds for all possible distributions of X_H .
- is the basis of state-of-the-art techniques for Quantitative Information Flow analysis.

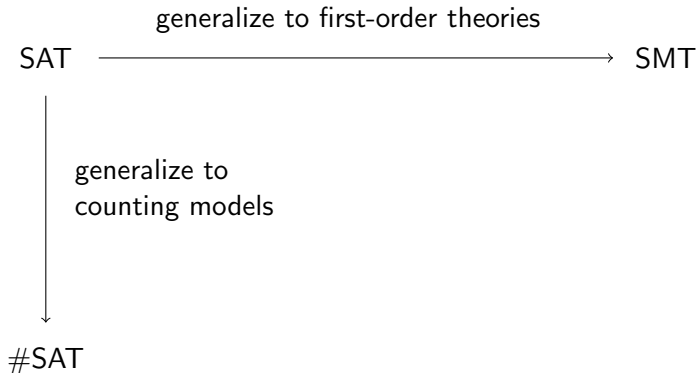
Definition

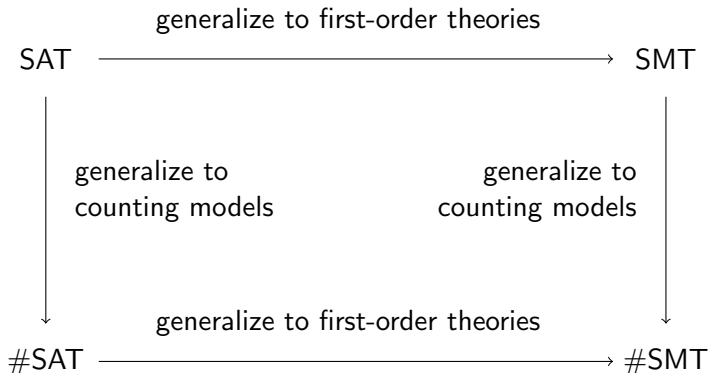
Quantitative Information Flow (QIF) is the problem of counting N , the number of possible outputs of a given program P .



- O is stored as a bit vector $b_1 b_2 \dots b_M$.
- Assume we have a first-order formula φ_P such that:
 - φ_P contains a set of Boolean variables $V_I := \{p_1, p_2, \dots, p_M\}$
 - $p_i = \top$ if and only if b_i is 1, and $p_i = \perp$ if and only if $b_i = 0$

Counting outputs of $P \equiv$ Counting models of φ_P w.r.t. V_I





DPLL Modulo Theories

$$\text{DPLL}(\mathcal{T}) = \text{DPLL} + \mathcal{T}\text{-solver}$$

$$\begin{aligned} \varphi := & \{\neg(x > 10) \vee A_1\} \wedge \\ & \{(x > 10) \vee \neg A_1\} \wedge \\ & \{\neg A_3 \vee (x < 1)\} \end{aligned}$$

$$\begin{aligned} \mathcal{BA}(\varphi) := & \{\neg B_1 \vee A_1\} \wedge \\ & \{B_1 \vee \neg A_1\} \wedge \\ & \{\neg A_2 \vee B_2\} \end{aligned}$$

$\mu^P = A_1 \wedge B_1 \wedge A_2 \wedge B_2 \Rightarrow \mathcal{T}\text{-solver}(\mu)$ returns **inconsistent**.

$\mu^P = A_1 \wedge B_1 \wedge \neg A_2 \wedge \neg B_2 \Rightarrow \mathcal{T}\text{-solver}(\mu)$ returns **consistent**.



Two approaches:

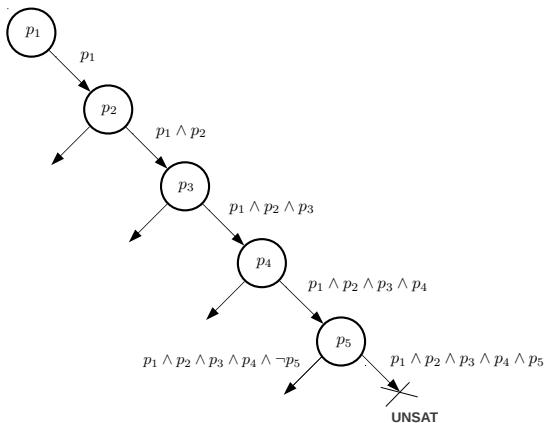
- Use formal methods to mimic $\text{DPLL}(\mathcal{T})$.
- Generate φ_P , then using $\text{DPLL}(\mathcal{T})$.

```
for all  $i$  from 1 to  $M$  do  
   $b_i = (0 \gg (i - 1)) \& 1$   
  if ( $b_i == 1$ ) then  
     $p_i \leftarrow \top$   
  else  
     $p_i \leftarrow \perp$ 
```

Figure : Program instrumentation to build the set V_i

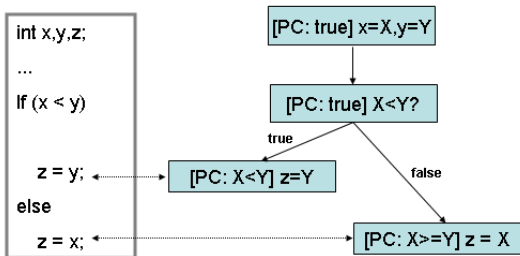
The algorithm consists of two components:

- A procedure to enumerate bit configurations (similar to DPLL)
- A model checker to check the existence of the bit configurations (similar to the \mathcal{T} -solver)



```
assert !(p1 && p2 && p3 && p4 && p5);
```

Symbolic Execution



- A program analysis technique that has several applications, in particular automated test generation.
- Executing programs with symbols instead of concrete inputs.

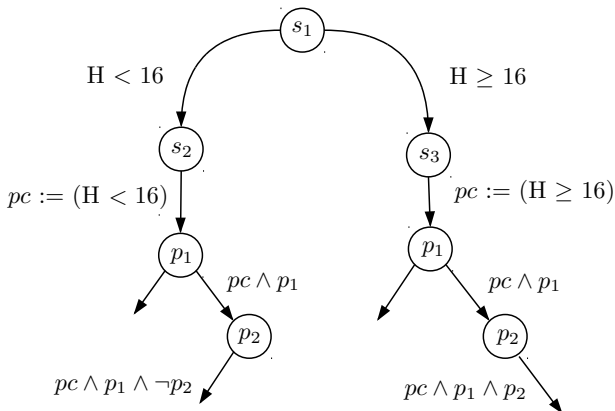
Symbolic Execution as DPLL Modulo Theories

$$\begin{aligned} \text{DPLL}(\mathcal{T}) &= \text{DPLL} + \mathcal{T}\text{-solver} \\ \text{Symbolic Executor} &= \text{Boolean Executor} + \mathcal{T}\text{-solver} \end{aligned}$$

Add conditions to test each bits of the output:

```
for all  $i$  from 1 to  $M$  do  
   $b_i = (0 \gg (i - 1)) \& 1$   
  if ( $b_i == 1$ ) then  
     $p_i \leftarrow \top$   
  else  
     $p_i \leftarrow \perp$ 
```

Figure : Program instrumentation to build the set V_i



- $(H \geq 16)$ and $(H < 16)$: program conditions.
- p_1, p_2, \dots : **additional conditions**.

Program transformation

```

L = 8;
if (H < 16)
  0 = H + L;
else
  0 = L;

```

```

(L1 = 8)           ∧
(G0 = H0 < 16)    ∧
(O1 = H0 + L1)   ∧
(O2 = L1)         ∧
(O3 = g?O1 : 03)

```

Figure : A simple program encoded into a first-order formula

Formula instrumentation to build the set V_I :

```

(assert (= (= #b1 ((_ extract 0 0) O3)) p1))

```

Use APIs provided by an SMT solver

Blocking clause

After finding a model

$$\mu = l_0 \wedge l_1 \wedge \dots \wedge l_m \wedge \dots$$

Add the clause:

$$\text{block} = \neg l_0 \vee \neg l_1 \vee \dots \vee \neg l_m$$

Depth-first search

Two components:

- A DPLL like procedure to enumerate truth assignments.
- Use the SMT solver to check consistency of the truth assignments.

- Tools selected:
 - Model Checking: CBMC (Ansi C)
 - Symbolic Execution: Symbolic PathFinder (Java bytecode)
 - Program transformation: CBMC
 - SMT solver: z3
- Benchmarks include:
 - Vulnerabilities in Linux kernel
 - Anonymity protocols
 - A Tax program from the European project HATS (Java)
- Assumptions: all programs have bounded loops, no recursion.

Some of the experiments:

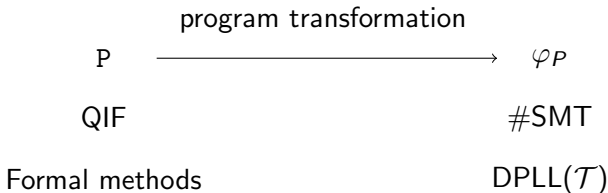
Case Study	Policy	LoC	sqifc time	selfcomp time
Data Sanitization	-	< 10	11.898	timed out
CVE-2011-2208	64	> 200	22.759	119.117
CVE-2011-2208	256		88.196	timed out
CVE-2011-1078	8	> 200	10.380	13.853
CVE-2011-1078	64		37.899	timed out
CRC	8	< 30	1.209	0.498
CRC	32		8.657	timed out

Figure : Times are in seconds, timeout is 30 minutes. In the first case study, “-” means the policy is not specified.

Some of the experiments:

Benchmark	Leaks	sqifc time	sqifc++ time		
			CBMC	aZ3	Total time
Data sanitization	4	11.898	0.165	0.086	0.251
Implicit flow	2.81	5.033	0.169	0.049	0.218
Population count	5.04	17.278	0.162	0.398	0.560
Mix and duplicate	16	-	0.154	136.947	137.101
Masked copy	16	-	0.175	18.630	18.805
Sum query	4.81	64.557	0.162	0.133	0.295
Ten random outputs	3.32	64.202	0.160	0.093	0.253
CRC (8)	3	2.551	0.184	0.099	0.283
CRC (32)	5	7.755	0.193	0.325	0.518

Figure : Leaks are in bits. aZ3 runs with the DFS-based algorithm. Times are in seconds, “-” means timeout in one hour. Total time of sqifc++ is the sum of CBMC time and aZ3 time.



Two approaches:

- Use formal methods to mimic $\text{DPLL}(\mathcal{T})$.
 - QIF analysis using Model Checking.
 - QIF analysis using Symbolic Execution.
- Generate φP , then using $\text{DPLL}(\mathcal{T})$.
 - Generate φP using program transformation.
 - Extend an SMT solver for $\#SMT$.

THANK YOU FOR YOUR ATTENTION!